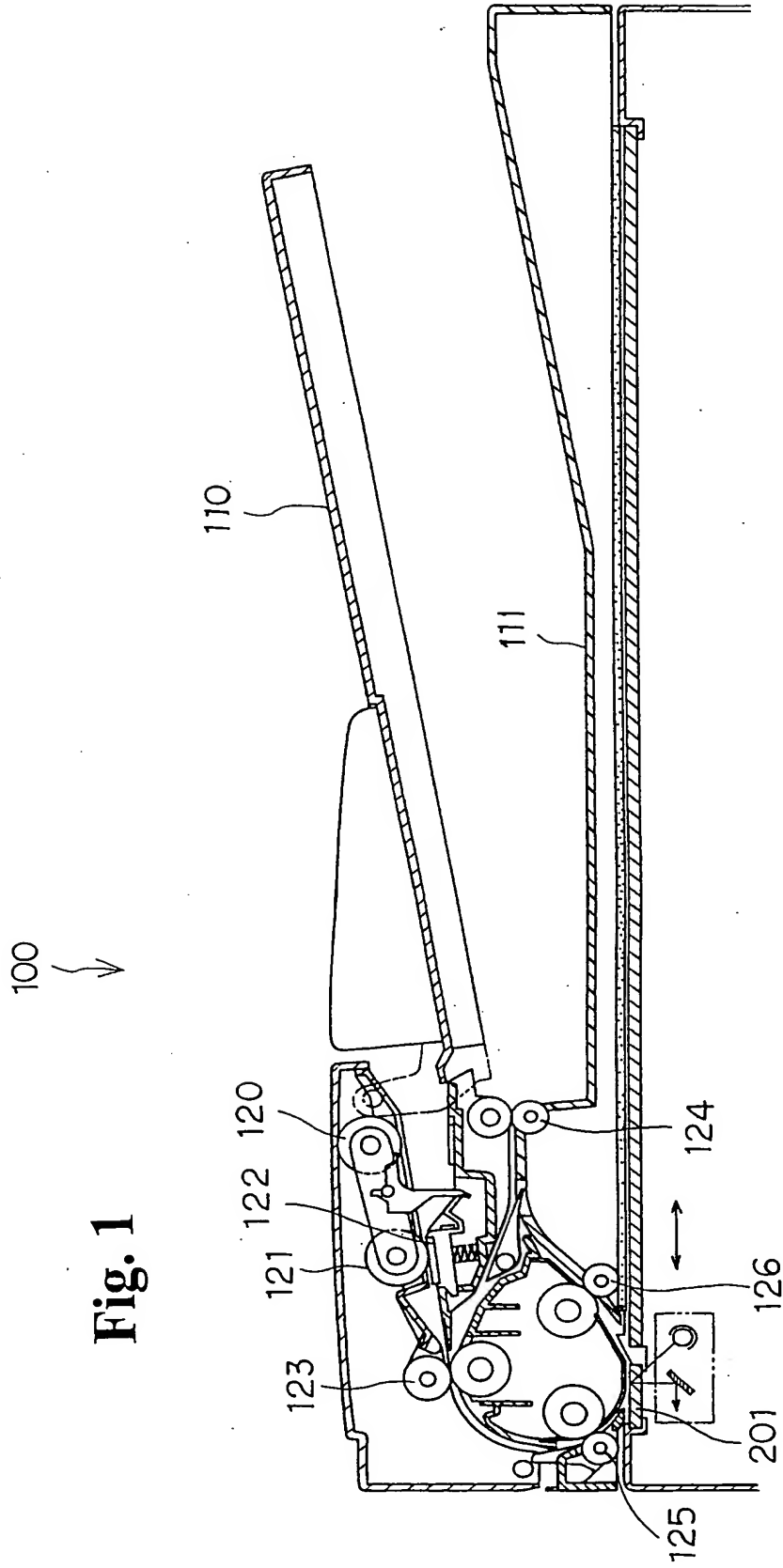
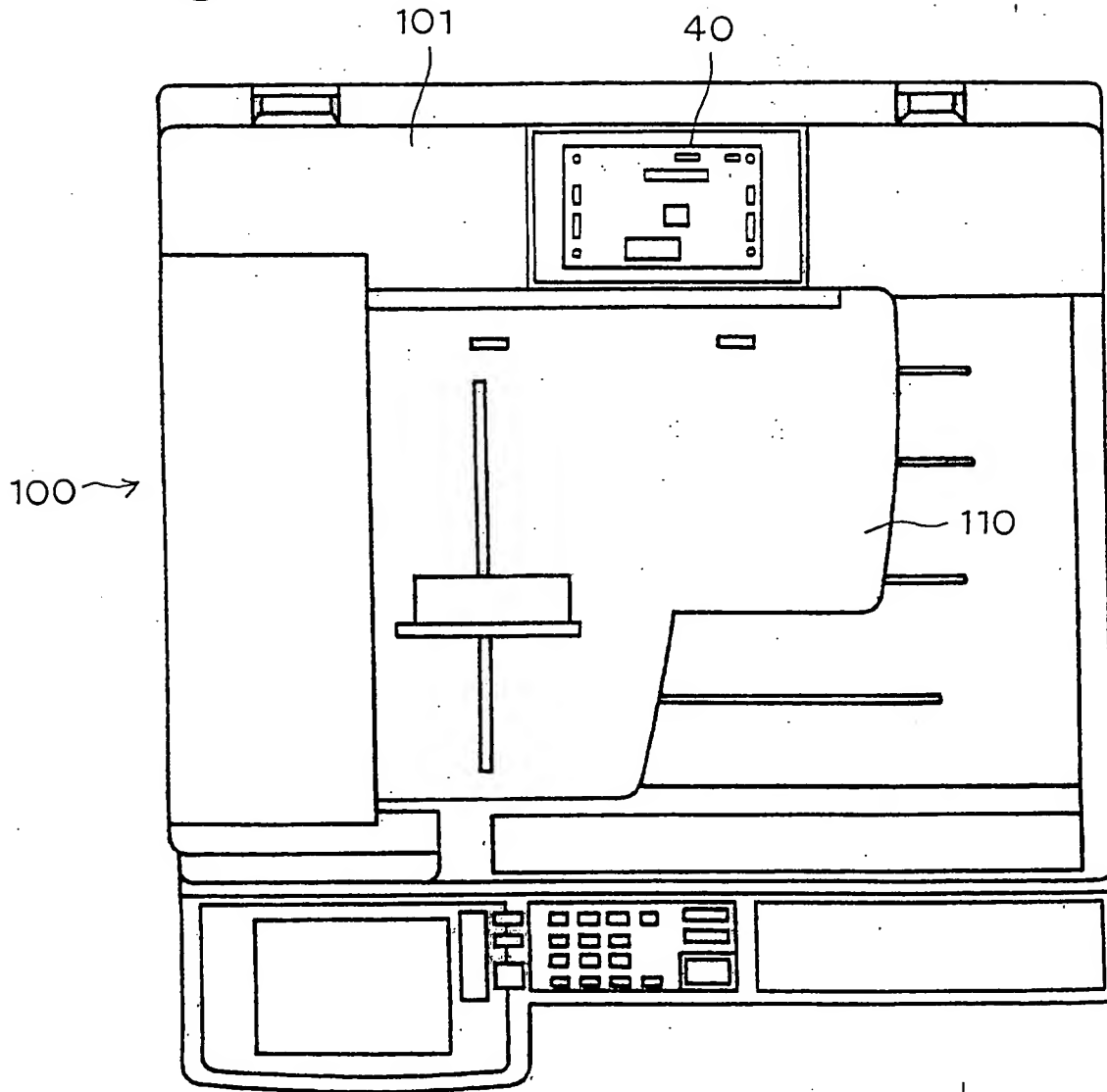


Fig. 1

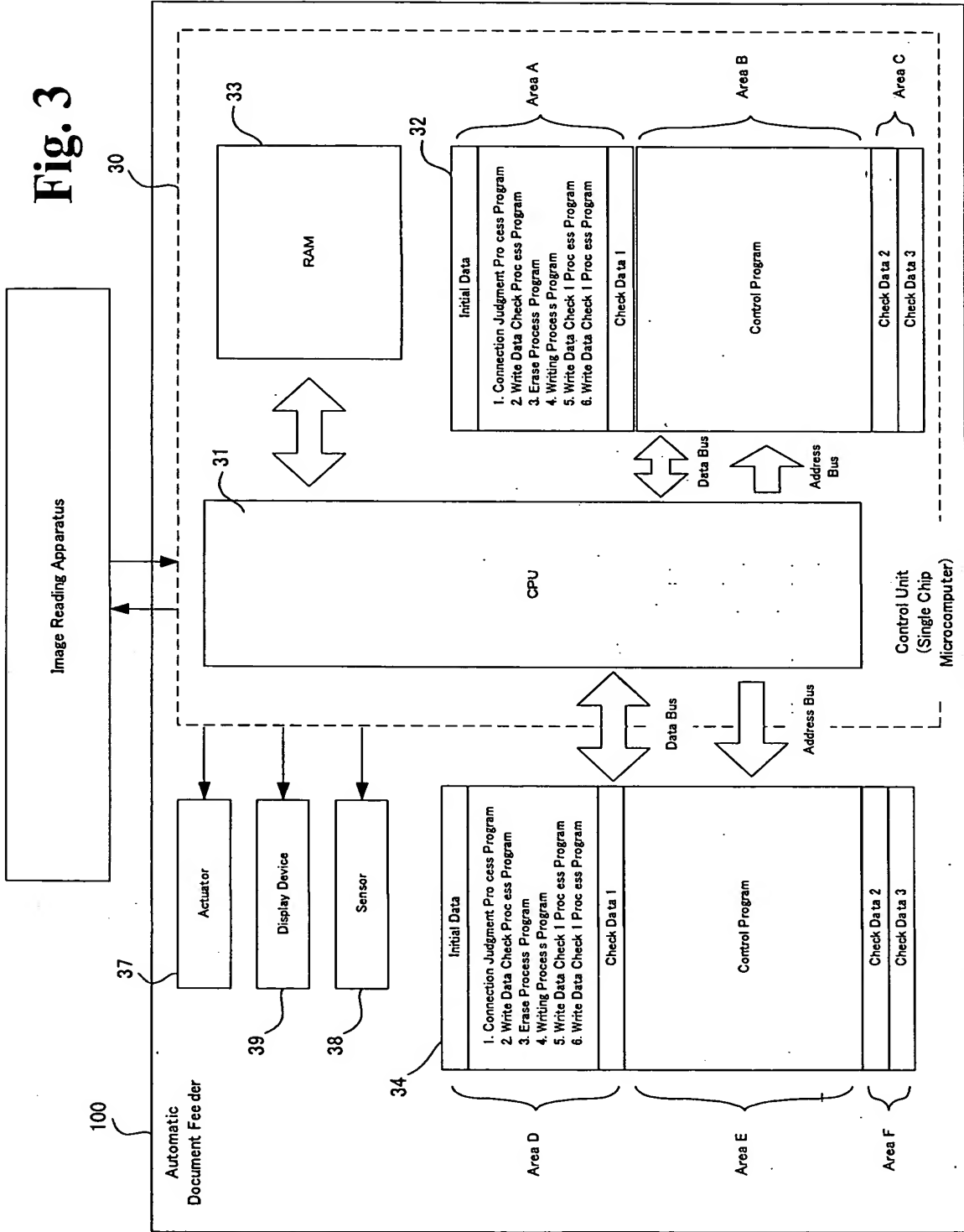


**Fig. 2**



D-1609  
3/12

Fig. 3



40



**Fig. 4**

D-1609  
5/12

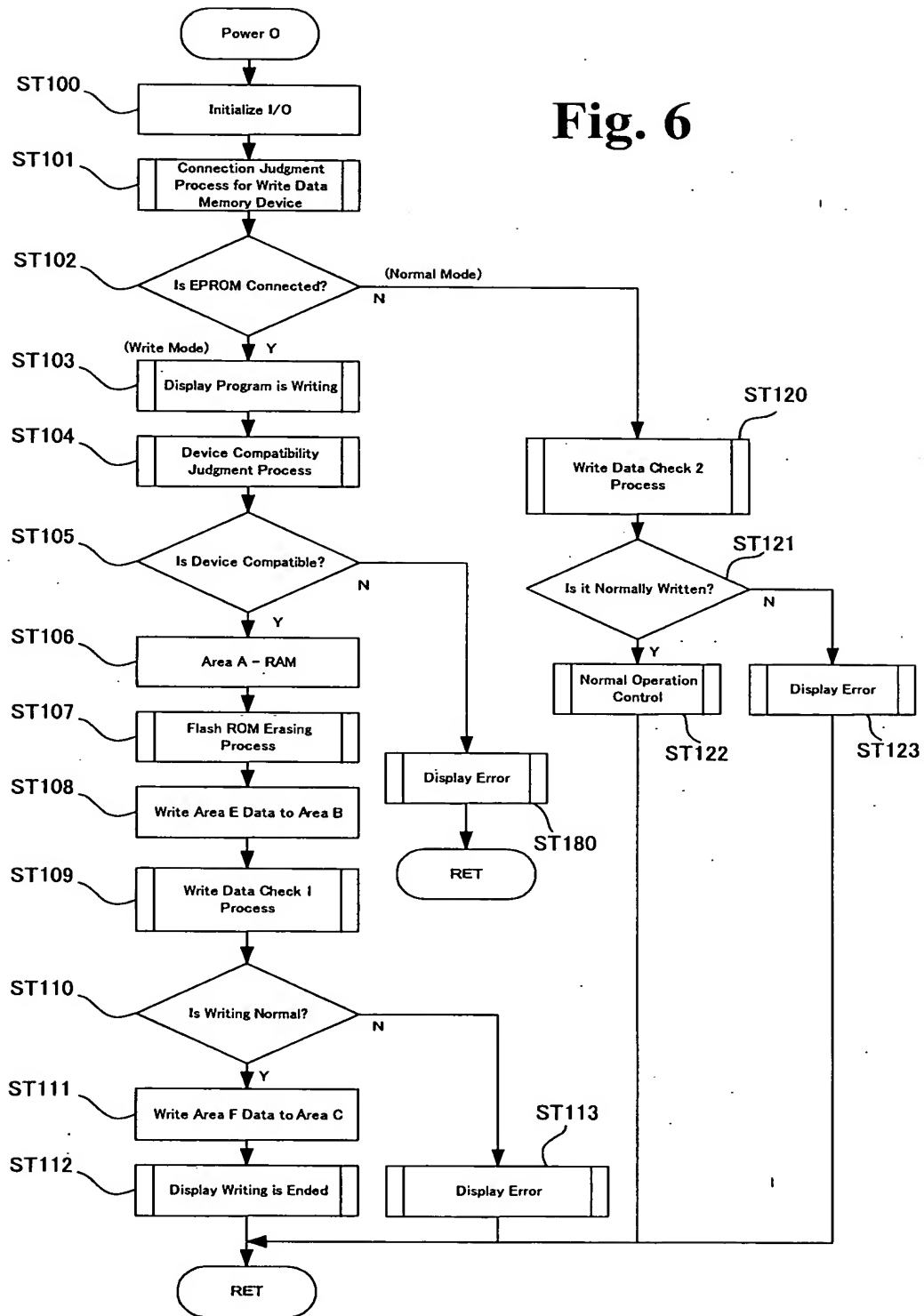
**Fig. 5**

Address Map

Addresses	Assigned Area	
0000~00FF	SFR Area	
0100~02FF	Internal RAM Area	
0300~03FF	Memory Area C	EEPROM Area
0400~08FF	Memory Area B	
0900~0FFF	Memory Area A	
1000~12FF	Not Used	
1300~13FF	Memory Area F	EPROM Area
1400~18FF	Memory Area E	
1900~1FFF	Memory Area D	

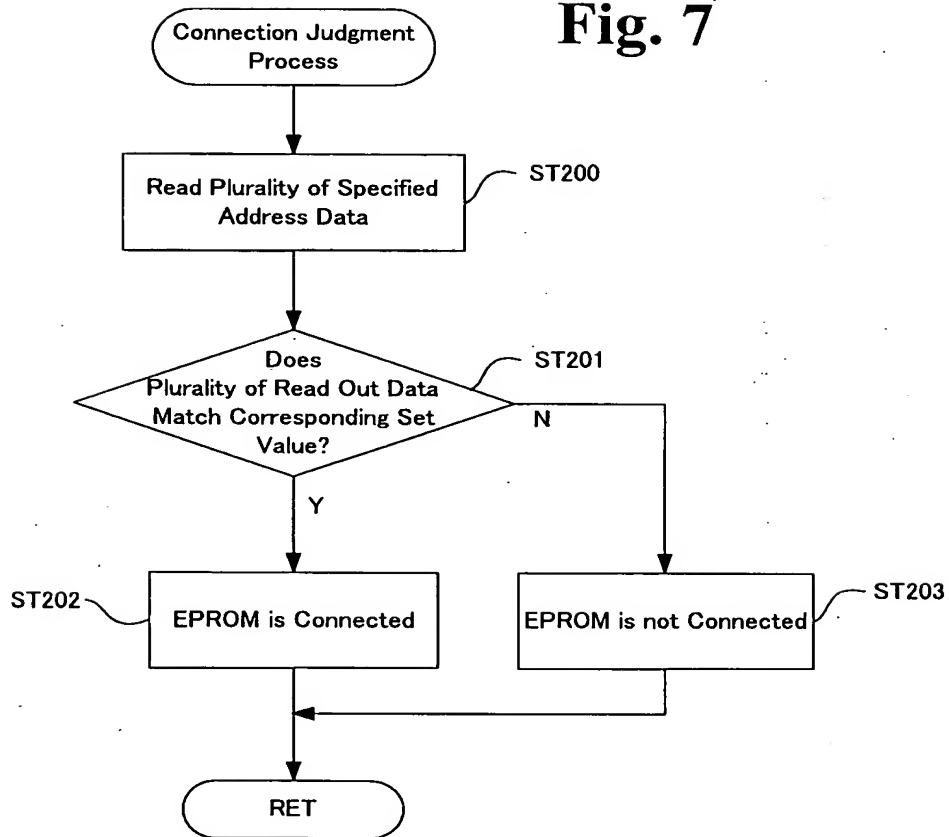
D-1609  
6/12

Fig. 6



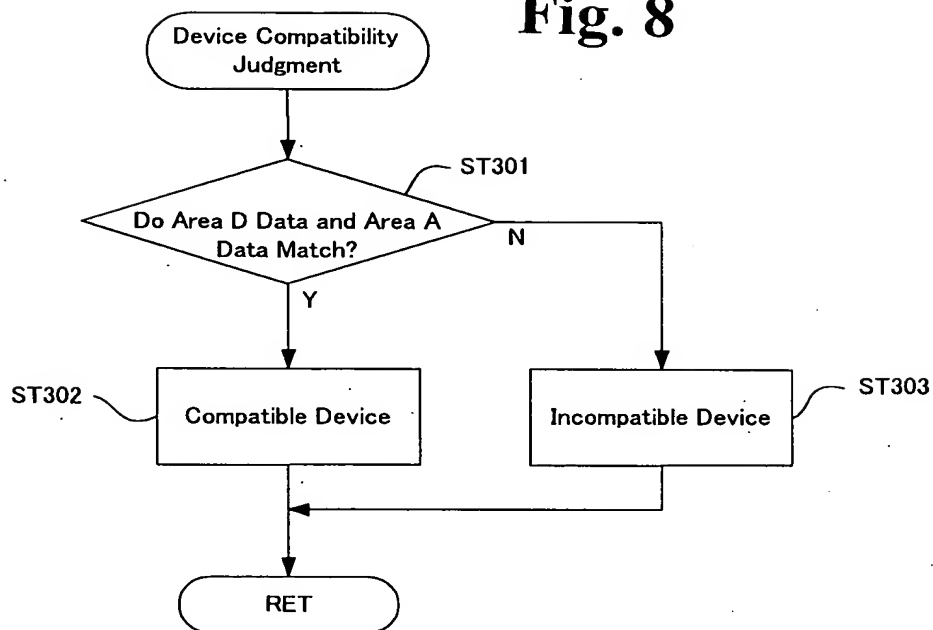
D-1609  
7/12

**Fig. 7**



D-1609  
8/12

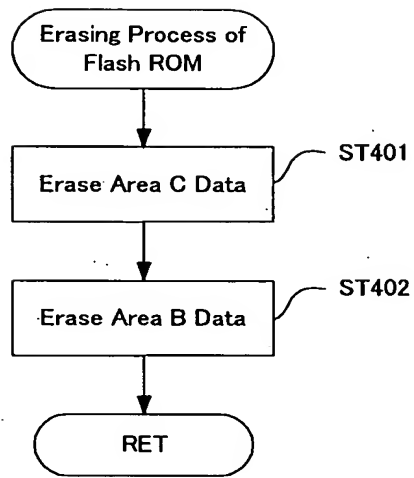
**Fig. 8**





D-1609  
9/12

**Fig. 9**



D-1609  
10/12

**Fig. 10**

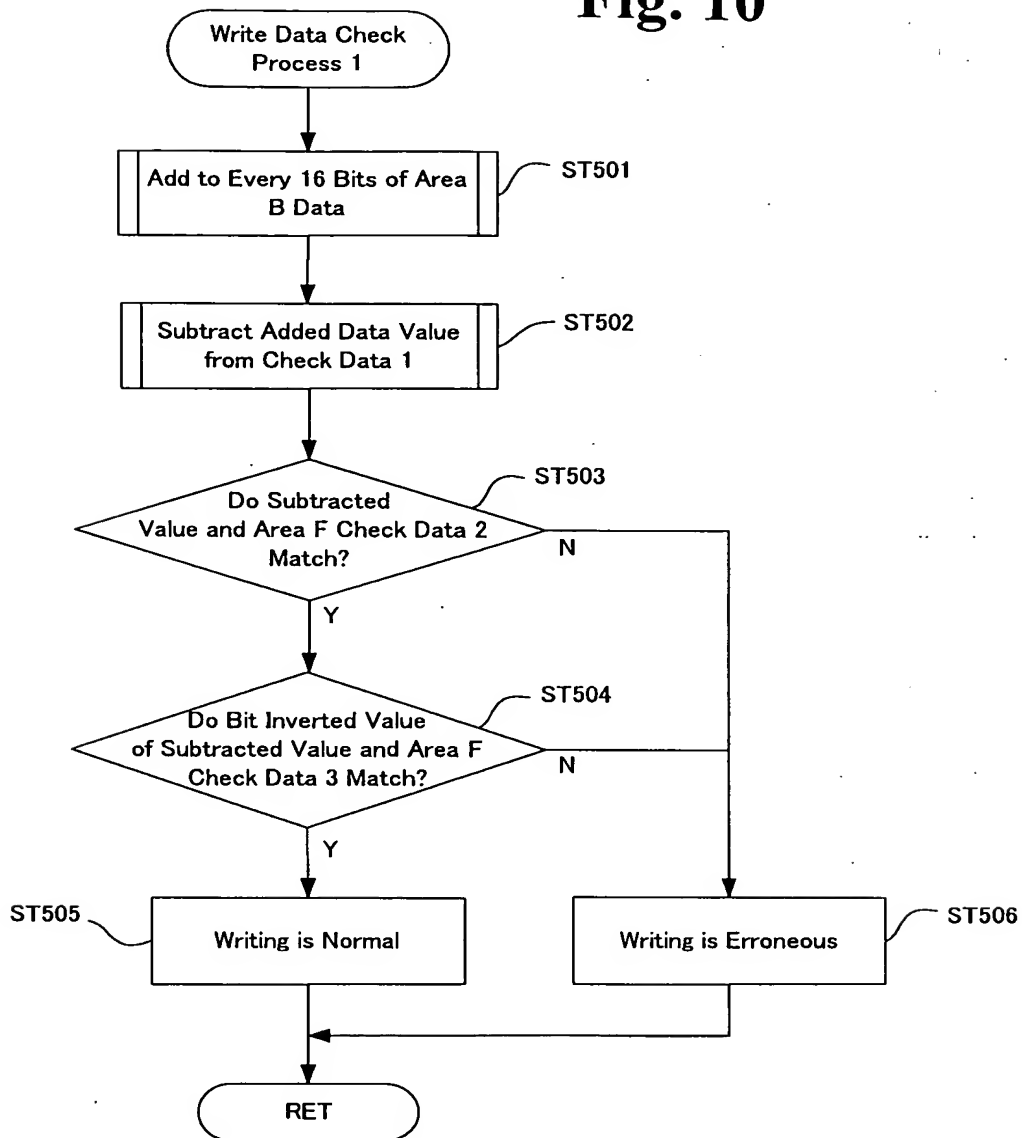
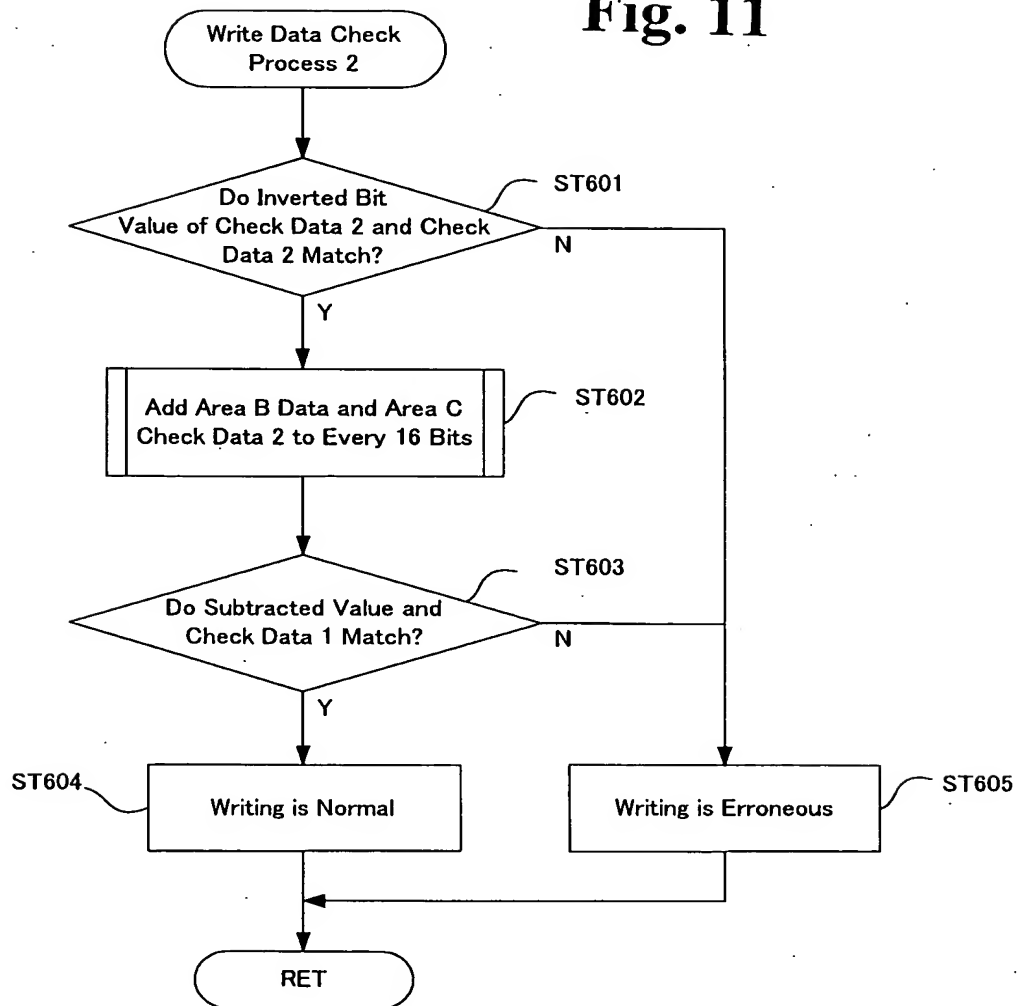


Fig. 11



D-1609  
12/12

Fig. 12

a d d	d a t a	
00	...	
01	...	
...	...	
0D	FF	
0F	FF	FFFF ←Check Data 1 (Sum of Address 10 to 1F+ Check Data 2)
=====		
a d d	d a t a	
10	01	
11	01	0101
<hr/>		
12	00	
13	05	0005
<hr/>		
14	EA	
15	01	EA01
<hr/>		
16	05	
17	10	1005
<hr/>		
18	00	
19	00	0000
<hr/>		
1A	C0	
1B	00	C000
<hr/>		
1C	31	
1D	10	3110
<hr/>		
1E	03	
1F	21	0321
<hr/>		
Sum Value 2AD3F⇒AD3F (Digits Beyond 16 Bits are Deleted.)		
=====		
20	52	
21	C0	52C0 ←Check Data 2
<hr/>		
22	AD	
23	3F	AD3F ←Bit Inverted Value of Check Data 2